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| APPLICATION NO.                   | FILING DATE     | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.     | CONFIRMATION NO. |  |  |
|-----------------------------------|-----------------|----------------------|-------------------------|------------------|--|--|
| 10/034,251                        | 01/03/2002      | Takaji Numao         | 1035-359                | 9405             |  |  |
| 75                                | 7590 06/29/2004 |                      |                         | EXAMINER         |  |  |
| NIXON & VANDERHYE P.C.            |                 |                      | NGUYEN, KEVIN M         |                  |  |  |
| 8th Floor<br>1100 North Glebe Rd. |                 |                      | ART UNIT                | PAPER NUMBER     |  |  |
| Arlington, VA 22201-4714          |                 | 2674                 | 8                       |                  |  |  |
|                                   |                 |                      | DATE MAILED: 06/29/2004 | 1                |  |  |

Please find below and/or attached an Office communication concerning this application or proceeding.

| •  |  |   |  |  |  |  |  |  |
|--|--|---|--|--|--|--|--|--|
|  |  | Applicat  | ion No.  | Applicant(s)   |  |  |  |  |
| Office Action Summary  |  | 10/034,2  | 251  | NUMAO, TAKAJI  |  |  |  |  |
|  |  | Examine   | er   | Art Unit   |  |  |  |  |
|  |  | Kevin M.  | Nguyen   | 2674   |  |  |  |  |
| The Period for Re  | ne MAILING DATE of this commun   | ication appears on th   | e cover sheet with the d   | orrespondence address  |  |  |  |  |
| A SHORT THE MAII - Extensions after SIX (i - If the perio - If NO perio - Failure to r Any reply r | ENED STATUTORY PERIOD F<br>LING DATE OF THIS COMMUN<br>of time may be available under the provisions<br>3) MONTHS from the mailing date of this commod<br>d for reply specified above is less than thirty<br>of d for reply is specified above, the maximum st<br>eply within the set or extended period for reply<br>eceived by the Office later than three months<br>ent term adjustment. See 37 CFR 1.704(b). | ICATION. s of 37 CFR 1.136(a). In no e nunication. s0) days, a reply within the statutory period will apply and very will, by statute, cause the ap | vent, however, may a reply be tin<br>atutory minimum of thirty (30) day<br>will expire SIX (6) MONTHS from<br>plication to become ABANDONE | nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133). |  |  |  |  |
| Status   |  |   |  |  |  |  |  |  |
| 1)⊠ Res  | sponsive to communication(s) file  | ed on <u>09 April</u> 2004.   |  |  |  |  |  |  |
|  |  | 2b)⊠ This action is non-final.  |  |  |  |  |  |  |
| 3)∐ Sin  | ce this application is in condition  | nis application is in condition for allowance except for formal matters, prosecution as to the merits is  |  |  |  |  |  |  |
| clos   | closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.  |   |  |  |  |  |  |  |
| Disposition of   | of Claims  |   |  |  |  |  |  |  |
| 4a)<br>5)⊠ Cla<br>6)⊠ Cla<br>7)□ Cla   | im(s) <u>2-14,16-19 and 23-25</u> is/ar<br>Of the above claim(s) is/a<br>im(s) <u>2-6,8,9,14 and 23</u> is/are all<br>im(s) <u>7,11-13,16-19,24 and 25</u> is<br>im(s) is/are objected to.<br>im(s) are subject to restrict  | re withdrawn from co<br>owed.<br>s/are rejected.  | onsideration.  |  |  |  |  |  |
| Application I  | Papers Papers  |   |  |  |  |  |  |  |
| 9) <u></u> The   | specification is objected to by th   | e Examiner.   |  |  |  |  |  |  |
| 10) <u></u> The  | drawing(s) filed on is/are:  | : a)  accepted or b   | ) ☐ objected to by the I   | Examiner.  |  |  |  |  |
| Арр  | licant may not request that any obje   | ction to the drawing(s)   | be held in abeyance. See   | e 37 CFR 1.85(a).  |  |  |  |  |
|  | placement drawing sheet(s) including   | ·   | -,,  | •  |  |  |  |  |
| II)∐ Ine   | oath or declaration is objected to   | o by the Examiner. N  | ote the attached Office  | Action or form PTO-152.  |  |  |  |  |
| Priority unde  | er 35 U.S.C. § 119   |   |  |  |  |  |  |  |
| a)⊠ A<br>1.⊠<br>2.⊑<br>3.⊑   | nowledgment is made of a claim    b) Some * c) None of:   Certified copies of the priority   Certified copies of the priority   Copies of the certified copies   application from the Internation  | documents have been documents have been of the priority documental Bureau (PCT Ru   | en received.<br>en received in Applicati<br>ents have been receive<br>le 17.2(a)).   | on No ed in this National Stage  |  |  |  |  |
|  |  |   |  |  |  |  |  |  |
| Attachment(s)  | Poforonoso Cita d (DTO DCC)  |   |  | (DTO 440)  |  |  |  |  |
|  | References Cited (PTO-892)<br>Draftsperson's Patent Drawing Review (F  | PTO-948)  | 4) Interview Summary Paper No(s)/Mail Da   |  |  |  |  |  |
| 3) 🛛 Informatio  | n Disclosure Statement(s) (PTO-1449 or<br>s)/Mail Date <u>2/21/02, 11/10/03</u> .  | PTO/SB/08)  |  | atent Application (PTO-152)  |  |  |  |  |



Art Unit: 2674

### DETAILED ACTION

- 1. The amendment filed on 04/09/2004 is entered. Claims 2-6, 8, 9, 14, 23 are allowed.
- 2. The indicated allowability of claims 7, 24, 10-13, 25, 16-19 is withdrawn in view of the newly discovered reference(s) to Okumura (US 5,945,972). Rejections based on the newly cited reference(s) follow.

# Allowable Subject Matter

- 3. Claims 2-6, 8, 9, 14, 23 are allowed.
- 4. The following is an examiner's statement of reasons for allowance: the cited prior arts, alone or in combination, do not teach or fairly suggest

"first switching element for each pixel, each first switching element being electrically connected at a first terminal thereof to one of the first wires; and

second switching element for each pixel, each second switching element being electrically connected in series with the memory means and a second terminal of the first switching element,

wherein, for a giving pixel in the display, the second terminal of the corresponding first switching element is electrically connected to the potential maintaining means," recited in claim 2.

"first switching element for each pixel, each first switching element being electrically connected at a first terminal thereof to one of the first wires and electrically connected at a second terminal thereof to the memory means; and

Art Unit: 2674

fourth switching element for each pixel, each fourth switching element being electrically connected at a first terminal thereof to one of the first wire and electrically connected at a second terminal thereof of the potential maintaining means," recited in claim 4.

# Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 6. <u>Claims 7, 24, 11-13, 25, 16-18 are rejected under 35 U.S.C. 102(b) as being anticipated by Okumura et al (US 5,945,972).</u>

As to claim 7, Okumura et al teaches a display comprising referring to eight embodiment, fig. 21, col. 24, lines 15-67

Liquid crystal display cells hereinafter CEL, col. 9, line 33-34, (electro-optic element);

PM 1, PM2 (memory means) and capacitor of CEL (potential maintaining means);

[recited in lines 5-9 of claim 7]

In the display panel on the receiving side, the adder 425 in each pixel adds an image signal stored in a corresponding one of the memory circuits PM1 and PM2 which store the same image signals stored in the memory circuits FM1 and FM2, respectively, and the difference signal externally transferred to recover the image signal. The selection

Art Unit: 2674

between the memory circuits PM1 and PM2 is made by the select signal driver 418 through the second memory selector 422, the select signal driver receiving the select signal from the fourth memory selector 402. If the recovered signal is background, the contents of a background memory are updated (fig. 21, col. 24, lines 56-67).

Thus, the display data is weighted between two memories PM1 and PM2 as claimed.

As to claim 24, Okumura et al teaches fig. 1A is a schematic illustration of a conventional LCD device, col. 8, lines 11-12, across the cell CEL is supplied a voltage corresponding to the difference between an applied potential from the corresponding pixel signal line and the potential of a common power supply VCOM (col. 9, lines 42-46).

As to claims 11, 16, Okumura et al teaches referring to fig. 3, liquid crystal display cells hereinafter CEL, col. 9, line 33-34, (electro-optic element);

PM 1, PM2 (memory means) and capacitor of CEL (potential maintaining means);

The first and second memories 121a and 121b of each cell receive an image signal over one of the image signal lines Lb1 to Lbn that corresponds to the column for that cell and hold it. The rewrite control of the memories is performed by the rewrite director 124 (col. 13, lines 39-43).

Thus, second memory means (121b), provided outside the pixel area (CEL), for recording a signal from which the electro-optic elements produce a display as claimed.

As to claims 12, 13, 17, 18, Okumura et al teaches the first and second memories 121a and 121b of each cell receive an image signal over one of the image

Art Unit: 2674

n

signal lines Lb1 to Lbn that corresponds to the column for that cell and hold it. The rewrite control of the memories is performed by the rewrite director 124 (col. 13, lines 39-43) by switching (selector 125, fig. 3) between multiple video images (memories 1-m, 121a-121m, fig. 5).

As to claim 25, Okumura et al teaches fig. 1A is a schematic illustration of a conventional LCD device, col. 8, lines 11-12, across the cell CEL is supplied a voltage corresponding to the difference between an applied potential from the corresponding pixel signal line and the potential of a common power supply VCOM (col. 9, lines 42-46).

7. Claim 10 is rejected under 35 U.S.C. 102(b) as being anticipated by Akiyama (US 5,952,991).

As to claim 10, Akiyama teaches a display comprising referring to fig. 1A, 2A, col. 5, lines 29-32, and lines 48-57.

a liquid crystal layer 105 (electro-optic element, fig. 2A), a memory portion (fig. 1A),

a display operation by the electro-optic elements (liquid crystal layer 105) is controlled using outputs from the memory means (capacitor C2, fig. 2A) and the potential maintaining means (capacitor C1, fig. 2A),

at least one transistor Tr2, 107 (sixth switching element fig. 2A) interposed between the potential maintaining means (said capacitor C1, fig. 2A) and Vac (power source wire, fig. 2A).

Art Unit: 2674

## Claim Rejections - 35 USC § 103

- 8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 9. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Okumura et al in view of Troutman (US 6,157,357).

As to claim 19, Okumura et al teaches the invention is applicable to any other display device that has pixels arranged in columns and rows such as an EL electroluminescent display (col. 28, lines 6-9).

Okumura et al does not teach organic LED elements.

Troutman teaches an organic LED 108 (fig. 1A, col. 2, lines 20-28).

It would have been obvious to a person of ordinary skill in the art at the time of the invention to substitute each Okumura's EL element including organic LED, in view of the teaching in the Troutman's reference because this would improve the quality of the image being displayed without visual artifacts if they exist as taught by Troutman (col. 3, lines 45-46).

#### Conclusion

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Kevin M. Nguyen** whose telephone number is **703-305-6209**. The examiner can normally be reached on MON-THU from 9:00-6:00.

Art Unit: 2674

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A Hierpe can be reached on 703-305-4709.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9306 (for Technology Center 2600 only)

Hand-delivered response should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Kevin M. Nguyen Patent Examiner Art Unit 2674

KN June 20, 2004

> AIKU WU PRIMARY EXAMINER